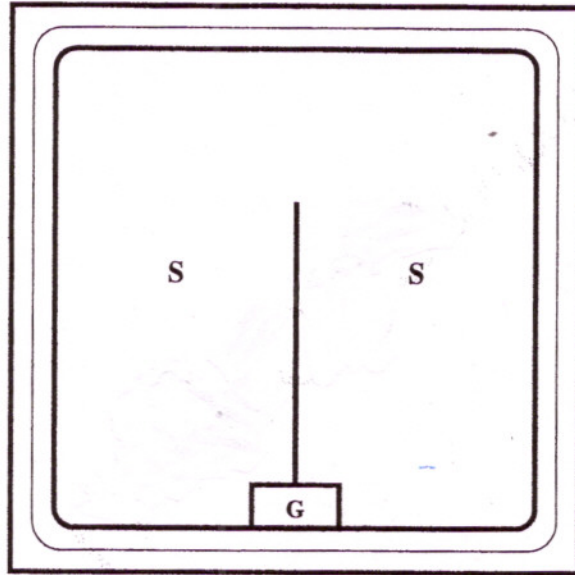




# Sierra Components, Inc.

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### PAD FUNCTIONS

G = GATE

S = SOURCE

BACK = DRAIN

The information given is believed to be correct at the time of issue.

Please verify your requirements prior to commencement of any assembly process, as no liability for omission or error can be accepted.

Back potential is the electrical potential of the substrate (bulk silicon), which may not require an electrical connection unless stated in this drawing.

**Note: 1 mil = 0.001inch**

<p><u>APPROVED</u></p> <p>GB</p> <p>.....</p> <p>DATE: 12/06/12</p>	<p style="text-align: center;"><b>FDP10N60N</b></p> <p style="text-align: center;"><b>FAIRCHILD SEMICONDUCTOR</b></p>	<p><u>DIE INFORMATION</u></p> <p>DIMENSIONS (Mils): 101 x 105 x 8</p> <p>BOND PADS (Mils): Gate = 13.6 x 9.7</p> <p>MASK REF:</p> <p>GEOMETRY:</p> <p>BACK POTENTIAL: DRAIN</p>
<p><u>SERIAL NUMBER</u></p> <p>M000777</p>		<p><u>METALLISATION</u></p> <p>TOP: Al</p> <p>BACK: Ti-Ni-Ag</p>